

**IN THE SPECIFICATION:**

Kindly amend the paragraph beginning on page 19, line 4, as follows:

Fig. 12 is another implementation of replication transmitter 30. Fig. 12 illustrates a transmitter comprising  $n$  direct drive programmable high speed power digital to analog converters  $400_1$ - $400_n$ . A complete description of such is provided in commonly assigned, copending application "Direct Drive Programmable Class A and B High Speed Power DAC", filed on even date and assigned application Serial No. 09/737,474 (Attorney Docket No. MP0035, now U.S. Patent No. 6,462,688), the contents of which are incorporated herein by reference. In accordance with IEEE standard 802.3ab the transmitter provides 17 different levels which is accomplished by superpositioning selected ones of the direct drive programmable high speed power digital to analog converters  $400_1$ - $400_n$ . In this arrangement the replication transmitter comprises current sources  $I_1 \dots I_n$  configured in series to develop an output voltage across  $R_{\text{replication}}$ . In this arrangement,  $R_{\text{replication}}$  may be adjustable similarly as described above. Alternatively,  $R_{\text{replication}}$  may be fixed and the output voltage may be multiplied by a voltage multiplier similar to that of voltage multiplier 200. Fig. 13 shows the details of one of the direct drive programmable high speed power digital to analog converters  $400_1$  and a detailed explanation of which can be found in the aforementioned commonly-assigned application.